

Indian Institute of Information Technology Tiruchirappalli



Chips to Startup Programme (C2S)

Special Manpower Development Program for Chips to System Design (SMDP-C2SD)

Category-II: Development of Application Oriented Working Prototype of IPs/ASICs/SoCs

Project Title: Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications

About Chips to Startup (C2S)

The Chips to Startup (C2S) Programme aims to train 85,000 number of high-quality engineers, Specialized Manpower in the area of Very large-scale integration (VLSI) and Embedded System Design, Development of ASICs (Application Specific Integrated Circuits), System on Chips (SoC) and IP Core over a period of 5 years.

C2S in the field of Electronics System Design & Manufacturing (ESDM) provides space by inculcating the culture of SoC/System Level Design at Bachelors, Masters and Research level and act as a catalyst for growth of Start-ups involved in fabless design.

In line with Prime Minister Shri Narendra Modi's plan to transform India into the next semiconductor hub, the Ministry of Electronics and Information (MeitY) has sought applications from academia, R&D organisations, start-ups and MSMEs under its Chips to Startup (C2S) Programme.

C-DAC (Centre for Development of Advanced Computing), a scientific society operating under MeitY, serves as the nodal agency for the programme.