Indian Institute of Information Technology Tiruchirappalli











Chips to Startup Programme (C2S)

Project Details

Special Manpower Development Programme for Chips to System Design (SMDP-C2SD)

Category-II: Development of Application Oriented Working Prototype of IPs/ASICs/SoCs

Project Title	Ultra-low-power Shakti's RISC-V based Lightweight Edge AI Processor for IoT enabled healthcare applications
Project Duration	2024 – 2029 (5 years)
Funding agency	Ministry of Electronics & Information Technology (MeitY), New Delhi-110003
Nodal Institute	National Institute of Technology Tiruchirappalli
Co-Participating	National Institute of Electronics & Information Technology (NIELIT),
Institute	Aurangabad
Total Project Outlay	Rs. 2.88/- Crore
IIITT Budget Outlay	Rs. 96/- Lakh