

SPEAKERS

Sessions will be handled by experts in the areas of VLSI, Embedded systems and IoT from academic Institutions, industries and R&D organizations.

WHO CAN ATTEND

FDP is open to Academicians, Research Scholars and PG Scholars from AICTE approved institutions & Central Government institutions.

EXPECTED OUTCOME OUTCOMES OF THE FDP

- Evolving advanced teaching and learning process in the field of VLSI and embedded system.
- Design and implementation of VLSI and Embedded system for IoT Applications.
- Enhance problem statements and possible solutions for the industry and research.

REGISTRATION FEE

No registrations fee will be collected from the participants.

CERTIFICATE

The digital certificates shall be issued to the participants who have attended the program with minimum 80% attendance and scored minimum 60% marks in the test.

ORGANISING COMMITTEE

Patron

- Dr. N V S N Sarma, Director ,IIIT Tiruchirappalli

Coordinator(s)

- Dr. G. Seetharaman, Associate Professor & Head, Department of ECE, IIIT Tiruchirappalli, Email id: jgsraman@gmail.com
- Dr. R. Dhanalakshmi, Associate Professor & Head, Department of CSE, IIIT Tiruchirappalli, Email id: dhanalakshmir@iiitt.ac.in



AICTE Training and Learning (ATAL) Academy



Sponsored

Online Faculty Development Programme

On

**Design Challenges in VLSI and Embedded system for
IoT Applications**

23.11.2020 to 27.11.2020



Department of ECE & CSE

Indian Institute of Information Technology Tiruchirappalli

(Oxford Engineering College Campus)

Tiruchirappalli- 620009.

www.iiitt.ac.in

ABOUT THE COLLEGE

The Indian Institute of Information Technology Tiruchirappalli (IIIT) was established in the year 2013-14 as the Institute with National Importance under Public Private Partnership Mode by Ministry of Human Resource Development (MHRD), Govt. of India. The Stakeholders of IIIT are Central Govt. of India, State Govt. of Tamil Nadu, and Industry partners, viz., TCS, CTS, Infosys, Ramco Systems, ELCOT, and Navitas. The main objective of IIIT is to impart world class education in Engineering and Technology to conduct research in the relevant fields, and to further advance learning and dissemination of knowledge. The Institute has been conferred with autonomy in financial and administrative matters to achieve rapid development.

A major objective in establishing IIIT is to set up a model of education which can produce best-in-class human resources in IT and can harness the multidimensional facets of IT in various domains. While the number of students produced would be small, the impact they create would be great. IIIT Tiruchirappalli is operating in the temporary campus within the premises of Oxford Engineering College campus, Tiruchirappalli - 620 009, Tamil Nadu from mid-July 2020.

ABOUT THE FACULTY DEVELOPMENT PROGRAMME

The Primary objective of the Online Faculty Development Programme is to understand the insights of Design Challenges in VLSI and Embedded Systems for IoT Application. Internet of Things (IoT) is a new revolution of things with the Internet. It is a network of physical devices, vehicles, home appliances, and other items embedded with electronics, software, sensors, actuators and network connectivity which are linked to create smart environments. Also, it is creating opportunities for more direct integration of the physical world into computer based systems, which results in improved efficiency and accuracy. The FDP will give the platform to understand and to use IoT in real-time applications. The program also motivates research ideas with practical applications by the experts in this field of research.

REGISTRATION

The interested candidates are required to register for the FDP through following link on or before the last date <https://www.aicte-india.org/atal>
The number of participants is limited to 200 and will be selected based on first come first serve basis and prerequisite needed. **For any clarification, the FDP coordinators can be contacted.**

SCHEDULED DATES

Last date for receipt of Registration form : **16.11.2020**
Confirmation to the participants through email : **19.11.2020**

COURSE MODULES

- Architecture of FPGA
- Features available with Xilinx and Altera FPGAs
- Design of SoCs
- Design of NoCs
- Low power Design
- High Speed techniques
- Pipelining and Parallel Processing
- Retiming
- Folding transformation Techniques
- Unfolding transformation Techniques
- IoT Basics
- Hardware and Software Components
- Cyber security
- Introduction to Embedded System
- Challenges in IoT Design
- Design principles of Various IoT Applications